

7/12

Notice of Allowability	Application No.	Applicant(s)	
	10/707,664	WU ET AL.	
	Examiner	Art Unit	
	Tarifur R. Chowdhury	2871	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--
 All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment filed on 01/26/06.
2. ☒ The allowed claim(s) is/are 1-12, 14-18 and 20.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| <ol style="list-style-type: none"> 1. <input type="checkbox"/> Notice of References Cited (PTO-892) 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | <ol style="list-style-type: none"> 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____ 7. <input type="checkbox"/> Examiner's Amendment/Comment 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance 9. <input type="checkbox"/> Other _____ |
|---|--|

Allowable Subject Matter

1. Claims 1-12, 14-18 and 20 are allowed.
2. The following is an examiner's statement of reasons for allowance:
3. As to claim 1, the prior arts of record do not anticipate or render obvious to one skilled in the art a dual mode liquid crystal display device comprising various elements as claimed, more specifically, a lower substrate comprising a reflective electrode connected to a first thin film transistor and a regional light-emitting source with a reflective cathode electrically connected to a second thin film transistor.

As to claim 9, the prior arts of record do not anticipate or render obvious to one skilled in the art a dual mode liquid crystal display device comprising various elements as claimed, more specifically, an upper substrate having a transparent electrode electrically connected to a first thin film transistor and a regional light-emitting source electrically connected to a second thin film transistor and a lower substrate having a bumpy layer thereon, a reflective layer on the bumpy layer, a color filtering layer on the reflective layer and a second transparent electrode on the color filtering layer.

As to claim 15, the prior arts of record do not anticipate or render obvious to one skilled in the art a dual mode liquid crystal display device comprising various elements as claimed, more specifically an upper substrate having a first transparent electrode and a regional light-emitting source electrically connected to the first transparent electrode and a lower substrate having a second transparent electrode thereon, wherein between the lower substrate and the second transparent electrode there is a bumpy layer on the

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surface of the lower substrate, a reflective layer on the bumpy layer and a color filtering layer array on the reflective layer.

The closest reference USPAT 6,714,268 is related to a dual-node liquid crystal display device having first and second transistors wherein a transparent electrode is connected to the first transistor and a regional light-emitting source is connected to the second thin film transistor. However, the above reference alone or in combination with any other prior art references fail to teach or suggest the claimed dual-mode liquid crystal display device.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

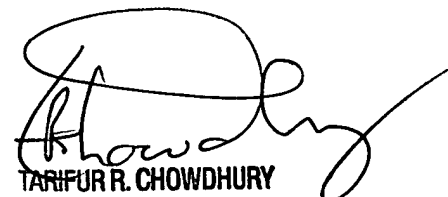
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tarifur R. Chowdhury whose telephone number is (571) 272-2287. The examiner can normally be reached on M-Th (6:30-5:00) Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim can be reached on (571) 272-2293. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TRC
February 01, 2006



TARIFUR R. CHOWDHURY
PRIMARY EXAMINER